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Subject:	Thomas E. Kobayashi - 10/075,218								
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EXAMINER: Dana Farahani									
GROUP ART UNIT: 2614									
SERIAL NO.: 10/075,218									
FILED: February 14, 2002									
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GROUP ART UNIT 2814
SERIAL NO.: 10/075,218
FILED: February 14, 2002
INVENTOR: Thomas S. Kobayashi

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*
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Thomas S. Kobayashi GROUP ART UNIT: 2814
APPLN. NO.: 10/075,218 EXAMINER: Dana Farahani
FILED: February 14, 2002
TITLE: SEMICONDUCTOR DEVICE HAVING A FUSE AND METHOD OF FORMING
THEREOF

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**NOTICE OF APPEAL FROM THE EXAMINER TO THE
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Commissioner for Patents
Alexandria, VA 22313

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 502117. This Notice is submitted in duplicate.

Respectfully submitted,
Thomas S. Kobayashi et al.

Tom Kobayashi

MOTOROLA, INC.
Customer Number: 23125

Agent for Applicant(s)
Reg. No. 50,714
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PATENT
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